

WEST Search History

[Hide Items](#) [Restore](#) [Clear](#) [Cancel](#)

DATE: Saturday, December 24, 2005

<u>Hide?</u>	<u>Set Name Query</u>	<u>Hit Count</u>
	<i>DB=JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L25 L24 and (pad\$ or pin\$ or terminal\$)	21
<input type="checkbox"/>	L24 L23 and (IC\$ or integrated circuit\$ or chip\$)	46
<input type="checkbox"/>	L23 (semiconductor or memory) and package adj2 (form or format)	87
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L22 L14 and package adj2 (form or format)	20
<input type="checkbox"/>	L21 L14 and package adj2 (form or format or configuration)	46
<input type="checkbox"/>	L20 L17 and (wire\$ or bond\$)	45
<input type="checkbox"/>	L19 L18 and (BGA or ball grid array)	11
<input type="checkbox"/>	L18 L14 and (TSOP or thin small outline package)	63
<input type="checkbox"/>	L17 L16 and control near2 (signal\$ or pulse\$ or command\$ or terminal\$)	70
<input type="checkbox"/>	L16 L15 and signal\$	79
<input type="checkbox"/>	L15 L14 and (TSOP or BGA)	80
<input type="checkbox"/>	L14 L13 and (arrangement or lay-out or layout or configuration)	483
<input type="checkbox"/>	L13 L12 and (package or housing)	523
<input type="checkbox"/>	L12 L11 and (command\$ or instruction\$) near4 (pin\$ or terminal\$ or pad\$)	1414
<input type="checkbox"/>	L11 L10 and address\$ near3 (pin\$ or terminal\$ or pad\$)	6080
<input type="checkbox"/>	L10 L3 and (data or I/O or IO) near3 (pin\$ or terminal\$ or pad\$)	20723
<input type="checkbox"/>	L9 L8 and control near2 signal	27
<input type="checkbox"/>	L8 L7 and (arrangement or lay-out or layout or configuration)	29
<input type="checkbox"/>	L7 L6 and (package or housing)	34
<input type="checkbox"/>	L6 L5 and (command\$ or instruction\$) near5 pad\$	105
<input type="checkbox"/>	L5 L4 and address\$ near2 pad\$	513
<input type="checkbox"/>	L4 L3 and (data or I/O or IO) near2 pad\$	3688
<input type="checkbox"/>	L3 L1 and (IC\$ or integrated circuit\$ or chip\$)	128052
<input type="checkbox"/>	L2 L1 and (IC\$ or integrated circuit\$)	95411
<input type="checkbox"/>	L1 semiconductor and memory	194753

END OF SEARCH HISTORY